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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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09/197,314 11/20/98 KRISHNAMACHARI

S PHA-23.543

EXAMINER

WM01/0719

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ART UNIT

PAPER NUMBER

2613

DATE MAILED:

07/19/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.
09/197,314

Applicant(s)

Krishnamachari

Examiner

Richard Lee

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on May 17, 2001.
- 2a) ☒ This action is FINAL. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-47 is/are pending in the application.
- 4a) Of the above, claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-47 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- 13) ☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☐ All b) ☐ Some* c) ☐ None of:

- ☐ Certified copies of the priority documents have been received.
- ☐ Certified copies of the priority documents have been received in Application No. _____.
- ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

*See the attached detailed Office action for a list of the certified copies not received.

- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

- 15) ☐ Notice of References Cited (PTO-892) 18) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 16) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 19) ☐ Notice of Informal Patent Application (PTO-152)
- 17) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____ 20) ☐ Other: _____

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1. The drawings are objected to for the same reasons as set forth in paragraph (2) of the last Office Action (see Paper no. 3). Though the applicant had indicated in the amendment filed May 17, 2001 that formal drawings have been submitted to overcome the objection, such drawings are not found in the case. It is apparent that the drawings have become detached from the file and are missing. Please resubmit the drawings for Examiner approval. The Examiner apologizes for any inconvenience that this may have caused.
2. Applicant is required to submit a proposed drawing correction in reply to this Office action. However, formal correction of the noted defect can be deferred until the application is allowed by the examiner.
3. Claims 8-10, 21-23, 34-36, 45, and 46 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The particular claim to the MPEG coding and MPEG coded bitstream as shown in the respective claims 8, 21, 34, 45, and 46 is indefinite because there are many versions of the MPEG recommendations and the recommends are continuously updated. The scope of the claim limitations cannot change over time, and unless the specification states the specific MPEG versions with the dates or a copy of the MPEG recommendation are provided, the claims are indefinite. Though the applicant had indicated in the amendment filed May 17, 2001 that the claims are considered definite since details of MPEG coding are describe in pages 7-8 of the

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present application, it is submitted again that the specific MPEG versions with dates or a copy of the MPEG recommendation be provided to overcome the rejection.

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-6, 8-19, 21-26, 40, 45, and 46 are rejected under 35 U.S.C. 102(b) as being anticipated by Ueno et al of record (5,418,570).

Ueno et al discloses a motion picture coding apparatus as shown in Figures 1, 7, 13, 15, and 18-21, and the same apparatus, method, and computer executable process steps stored on a computer readable medium as claimed in claims 1-6, 8-19, 21-26, 40, 45, and 46, the computer executable process steps to increase a resolution of at least a portion of a reference frame of video (see 35 of Figure 13, 60 of Figure 15), the apparatus, method, and computer executable process comprising the same selecting a first block of pixels in the reference frame (see columns 1-2, column 15, lines 33-41, column 16, lines 8-20); locating in N target frames one or more blocks of pixels that substantially correspond to the first block of pixels, where the N target frames are separate from the reference frame (i.e., search range calculation within coding section 30 of Figure 13, see column 15, lines 33-41); determining values of additional pixels based on values of pixels in the first block and on values of pixels in the one or more blocks, adding the additional pixels among the pixels in the first block, determining the values of the additional pixels based also

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on coefficients which are weighted in accordance with the first block and the one or more blocks, wherein the coefficients are weighted based on differences between pixels in the first block and pixels in each of the one or more blocks, wherein the differences comprise a residual (i.e., 35 of Figure 13, 60 of Figure 15, Figures 18-21, and see columns 1-2, columns 7-8, columns 15-16, columns 19-21); wherein the N target frames comprise frames of video which were predicted at least in part based on pixels in the reference frame (see column 16, lines 8-20); wherein in a case that the locating step does not locate any blocks of pixels in the target frames that substantially correspond to the first block of pixels, the determining step determines the values of the additional pixels based on values of pixels in the first block without regard to values of pixels in the N target frames (i.e., I frame processing, see column 2, lines 31-39); changing distances between pixels in the first block in order to change a size of the first block (see Figures 18-21, column 19-21); wherein the locating step uses motion vectors from the reference frame to the target frame to locate the one or more blocks of pixels and searches through the N target frames to locate the one or more blocks of pixels (see column 15, lines 33-41); wherein in a case that the reference and target frames of video are coded using MPEG coding, the locating step locates one or more blocks using motion vectors present in an MPEG coded bitstream for target frames and wherein the coefficients are determined using DCT values of at least one coded residual, where the at least one coded residual comprises differences between the reference frame and the target frames (see column 15, lines 33-41, column 20, lines 54-68, Figures 13, 15, 18-21); and wherein the reference frame comprises a B frame, and wherein before the selecting step, the step of determining a

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location of the first block in the reference frame based on blocks of pixels in frames which precede and which follow the reference frame (i.e., bidirectional prediction, see column 16, lines 8-20); and wherein the reference frame comprises one of an I frame and P frame, and wherein the N target frames comprise at least one of a P frame and a bi-directional frame (see column 2, lines 31-39, column 16, lines 8-20).

6. Claims 41-43 are rejected under 35 U.S.C. 102(b) as being anticipated by Yonemitsu et al of record (5,475,435).

Yonemitsu et al discloses a layer encoding and decoding apparatus as shown in Figures 1 and 2, and the same television system which receives coded video data (see column 1 and Figure 2), and which forms images based on the coded video data, the television system comprising a decoder (see Figure 2) which decodes the video data to produce frames of video; a processor (55, 57 of Figure 2) which increases a resolution of a reference frame of video based on pixels in the reference frame and based on pixels in at least one other target frame of the video, wherein the processor increases the resolution of the reference frame by selecting blocks of pixels in the reference frame and for each selected block, locating in N target frames one or more blocks of pixels that substantially correspond to the first block of pixels, where the N target frames are separate from the reference frame (see column 6, line 34 to column 7, line 7, line 63), and determining values of additional pixels based on values of pixels in the selected block and on values of pixels in the one or more blocks and adding the additional pixels among the pixels in the selected block (i.e., interpolation within upsampling circuit 57 of Figure 2); a display which

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displays an image based on the reference frame (see column 1 and Figure 2); and wherein in a case that the processor does not locate any blocks of pixels in the target frames that substantially correspond to the selected block of pixels, the processor determines the values of the additional pixels based on values of pixels in the selected block without regard to values of pixels in the N target frames (i.e., I frame processing as determined from the macroblock type signal, see column 2, lines 55-62, column 6, lines 34-44).

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 7 and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ueno et al as applied to claims 1-6, 8-19, 21-26, 40, 45, and 46 in the above paragraph (5), and further in view of Guetz et al (6,091,777).

Ueno et al discloses substantially the same apparatus, method, and computer executable process steps as above, but does not particularly disclose determining the values of the additional pixels by performing bilinear interpolation using at least some of the pixels in the first block as claimed in claims 7 and 20. However, Guetz et al discloses a continuously adaptive digital video compression system and teaches the conventional use of bilinear interpolation associated with motion estimation of blocks (see column 2, lines 23-47). Therefore, it would have been obvious to one of ordinary skill in the art, having the Ueno et al and Guetz et al references in front of

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him/her and the general knowledge of the interpolation of images, would have had no difficulty in providing the bilinear interpolation of image data as taught by Guetz et al as part of the motion estimation of pixel data within the system of Ueno et al for the same well known image interpolation purposes as claimed.

9. Claims 27-32, 34-39, and 47 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ueno et al as applied to claims 1-6, 8-19, 21-26, 40, 45, and 46 in the above paragraph (5), and further in view of Lempel of record (6,163,576).

Ueno et al discloses substantially the same apparatus, method, and computer executable process steps as above, but does not particularly a memory which stores computer executable process steps as claimed in claims 27. However, Lempel discloses a video encoder having reduced memory bandwidth requirements, and teaches the conventional use of a CPU 202 of Figure 2 for storing and executing computer processes. Therefore, it would have been obvious to one of ordinary skill in the art, having the Ueno et al and Lempel references in front of him/her, would have had no difficulty in providing the computer memory storage of executable processes as taught by Lempel for the video image processing system of Ueno et al for the same well known purposes as claimed.

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10. Claim 33 is rejected under 35 U.S.C. 103(a) as being unpatentable over the combination of Ueno et al and Lempel as applied to claims 1-6, 8-19, 21-32, 34-39, 40, and 45-47 in the above paragraphs (5) and (9), and further in view of Guetz et al of record (6,091,777).

The combination of Ueno et al and Lempel discloses substantially the same apparatus, method, and computer executable process steps as above, but does not particularly disclose determining the values of the additional pixels by performing bilinear interpolation using at least some of the pixels in the first block as claimed in claim 33. However, Guetz et al discloses a continuously adaptive digital video compression system and teaches the conventional use of bilinear interpolation associated with motion estimation of blocks (see column 2, lines 23-47). Therefore, it would have been obvious to one of ordinary skill in the art, having the Ueno et al, Lempel, and Guetz et al references in front of him/her and the general knowledge of the interpolation of images, would have had no difficulty in providing the bilinear interpolation of image data as taught by Guetz et al as part of the motion estimation of pixel data within the system of Ueno et al for the same well known image interpolation purposes as claimed.

11. Claim 44 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yonemitsu et al as applied to claims 41-43 in the above paragraph (6), and further in view of Song et al of record (6,115,070).

Yonemitsu et al discloses substantially the same television system as above, but does not particularly disclose wherein the decoder and the processor are implemented in a settop box as claimed in claim 44. Such decoder and processing within a settop box is however old and well

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recognized in the art, as exemplified by Song et al (See Figure 24 and column 24, line 64 to column 25, line 18. Therefore, it would have been obvious to one of ordinary skill in the art, having the Yonemitsu et al and Song et al references in front of him/her and the general knowledge of settop box functions, would have had no difficulty in providing the settop box with decoder and processing capabilities as taught by Song et al for the video image system of Yonemitsu et al for the same well known MPEG compliant decoding purposes as claimed.

12. Regarding the applicant's arguments from the amendment filed May 17, 2001 concerning that "...it is evident that Ueno et al only discloses searching for a motion vector. Therefore, it is respectfully submitted that the presently recited "locating, in target frames, one or more blocks of pixels that substantially correspond to the first block of pixels" is not anticipated by Ueno et al", the Examiner respectfully disagrees. It is submitted that the search range calculations for providing a motion vector reads on the "locating, in target frames, one or more blocks of pixels that substantially correspond to the first block of pixels" feature as claimed. In other words, the particular process in obtaining the motion vector involves the estimation of motion between two consecutive frames via block matching (see column 15, lines 33-41).

Regarding the applicant's arguments from the amendment filed May 17, 2001 concerning that "... it is respectfully submitted that the claims recite features not taught by Yonemitsu et al. In particular such features include "a processor which increases a resolution of a reference frame of the video based on pixels in the reference frame and based on pixels in at least one other target frame", as recited in claim 40 ... Yonemitsu et al only discloses the output picture of the lower

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layer is inputted to the up sampling circuit so that it can be converted into a non-interlace picture ...”, the Examiner respectfully disagrees. Since the upsampling circuit 55, 57 of Yonemitsu provides pictures from the upper and lower layers to produce non-interlace interpolation picture (see column 3, lines 50-58, column 7, lines 11-18), it is submitted that the non-interlace interpolation picture as outputted from up sampling circuit 57 provides the same “processor which increases a resolution of a reference frame of the video based on pixels in the reference frame and based on pixels in at least one other target frame” as claimed.

13. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

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14. Any response to this final action should be mailed to:

Box AF

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 308-9051, (for formal communications; please mark "EXPEDITED
PROCEDURE")

Or:

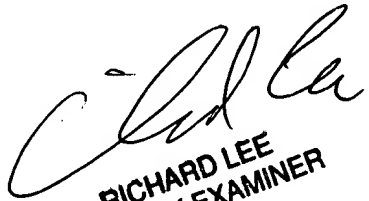
(703) 308-6306 (for informal or draft communications, please label
"PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive,
Arlington, VA., Sixth Floor (Receptionist).

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15. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard Lee whose telephone number is (703) 308-6612.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-4700.


RICHARD LEE
PRIMARY EXAMINER

Richard Lee/rl

7/17/01

